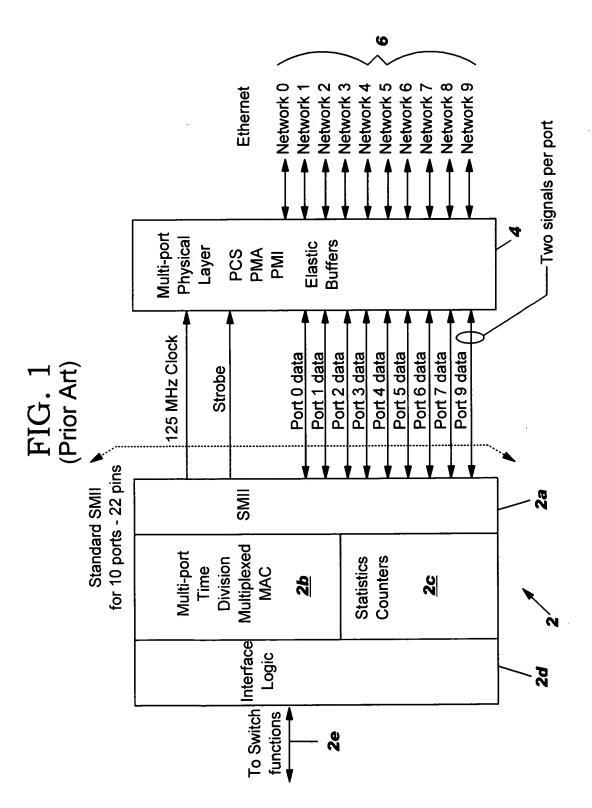
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Port Multiplexing to Reduce VO Pin Count



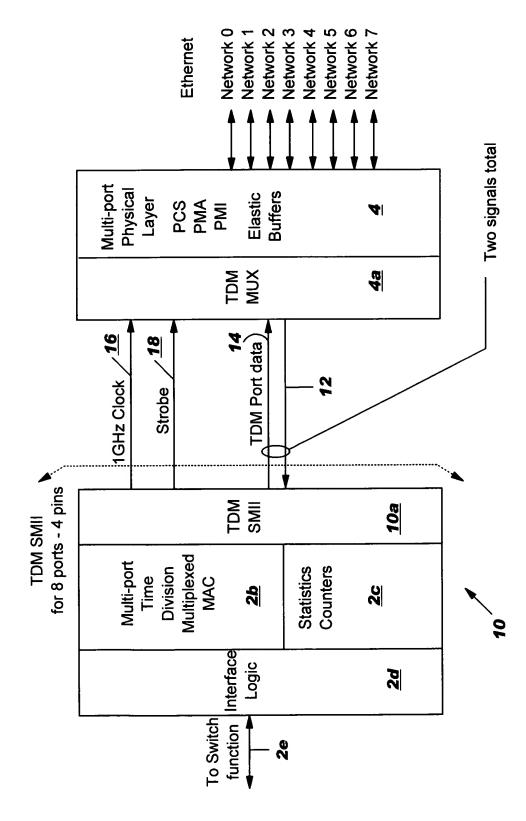


FIG. 2

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2n+1 2n+2 2n+3 × × × : n+4 × n+3 × n+2 × Clock × × \sqsubseteq × : • • • × က 0 × × Clock 0 × network n network 0 ~ က network 1 network network strobe

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